

Applicant : Adiletta et al.
Serial No. : 10/615,500
Filed : 7/8/2003
Page : 4

Attorney's Docket No.: 10559-075002
Intel Docket No.: P7567C

REMARKS

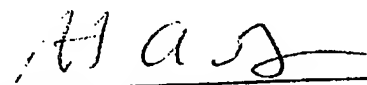
Claims 36-41 remain pending with claims 36, 38, and 40 being independent.

The Examiner previously rejected claim 36 under 35 U.S.C. 103(a) using Hwang (U.S. 6,216,220) as the primary reference. Hwang describes a system where instructions from thread context blocks 2 (FIG. 3 of Hwang) are transferred to functional units 4-1 (FIG. 3 of Hwang) for execution. That is, the thread context blocks 2 of Hwang do not execute the threads, the functional units 4-1 do. As amended, claim 36 recites that each of the multiple multi-threaded processing units executes the multiple threads. Hwang does not teach such processing units. Independent claims 38 and 40 have been similarly amended. Applicants respectfully request withdrawal of the Examiner's rejection of the claims.

If any fees are due, please apply such fees to Deposit Account No. 06-1050 referencing attorney docket number: 10559-075002.

Respectfully submitted,

Date: 2/21/06


Robert A. Greenberg
Reg. No. 44,133
Phone: 978-553-2060